



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sheu, *et al.* Docket No.: TSM03-0140  
Serial No.: 10/619,828 Art Unit: 2829  
Filed: July 15, 2003 Examiner: Dana Farahani  
For: Self-Aligned MOSFET having an Oxide Region below the Channel

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Dear Sir:

Applicants wish to bring to the attention of the U.S. Patent and Trademark Office the information noted on the enclosed PTO Form PTO/SB/08A & 08B, which may be considered material to the examination of the above-identified application.

This Information Disclosure Statement is submitted under 37 C.F.R. §1.97(c) together with a \$180.00 fee under 37 C.F.R. §1.17(p) after the C.F.R. §1.97(b) time period, but before final action or notice of allowance, whichever occurs first.

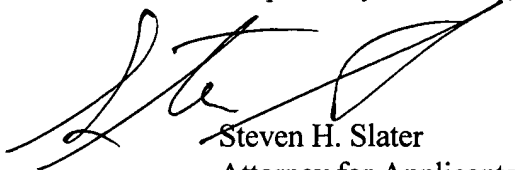
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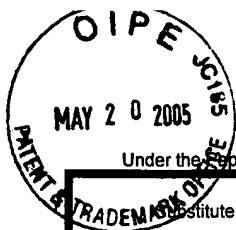
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May 16, 2005

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet

1

of

2

**Complete if Known**

Application Number	10/619,828
Filing Date	7/15/2003
First Named Inventor	Sheu, et al.
Art Unit	2829
Examiner Name	Farahani, Dana
Attorney Docket Number	TSM03-0140

**U.S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code <sup>2</sup> (if known)			
	1	US-4,069,094	01-17-1978	Shaw et al.	
	2	US-4,497,683	02-05-1985	Celler et al.	
	3	US-5,155,571	10-13-1992	Wang et al.	
	4	US-5,378,919	01-03-1995	Ochiai	
	5	US-5,479,033	12-26-1995	Baca et al.	
	6	US-5,789,807	08-04-1998	Correale Jr.	
	7	US-6,111,267	08-29-2000	Fischer et al.	
	8	US-6,256,239 B1	07-03-2001	Akita et al.	
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	18	US-6,720,619 B1	04-13-2004	Chen et al.	
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	29	US-5,273,915	12-28-1993	Hwang et al.	
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	32	US-6,100,153	08-08-2000	Nowak et al.	
	33	US-6,281,059	08-28-2001	Chen et al.	
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	36	US-5,708,288	01-13-1998	Quigley et al.	
	37	US-5,955,766	09-21-1999	Ibi et al.	
	38	US-6,521,952	02-18-2003	Ker et al.	
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	43	US-2002/0031890 A1	03-14-2002	Watanabe et al.	
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	45	US-2002/0125471 A1	09-12-2002	Fitzgerald et al.	
	46	US-2004/0026765 A1	02-12-2004	Currie et al.	
	47	US-2004/0173815 A1	09-09-2004	Yeo et al.	

Examiner  
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Considered

Substitute for form 1449A/PTO				<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)				Application Number	10/619,828
				Filing Date	7/15/2003
				First Named Inventor	Sheu, <i>et al.</i>
				Art Unit	2829
				Examiner Name	Farahani, Dana
Sheet	2	of	2	Attorney Docket Number	TSM03-0140

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)				
	48	EP 0 683 522 A2	11-22-1995	International Business Machines Corporation		
	49	EP 0 828 296 A2	03-11-1998	International Business Machines Corporation		

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	50	LEITZ, C.W., <i>et al.</i> , "Channel Engineering of SiGe-Based Heterostructures for High Mobility MOSFETs," Materials Research Society Symposium Proceedings, Vol. 686, 2002, pp. A3.10.1-A3.10.6.	
	51	LEITZ, C.W., <i>et al.</i> , "Hole Mobility Enhancements In Strained Si/Si <sub>1-y</sub> Ge <sub>y</sub> P-Type Metal-Oxide-Semiconductor Field-Effect Transistors Grown On Relaxed Si <sub>1-x</sub> Ge <sub>x</sub> (x<y) Virtual Substrates," Applied Physics Letters, Vol. 79, No. 25, December 17, 2001, pp. 4246-4248.	
	52	LIU, K.C., <i>et al.</i> , "A Novel Sidewall Strained-Si Channel nMOSFET," IEDM, 1999, pp. 63-66.	

Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.